**University of Miami**

**EEN 414 Computer Organization and Design**

**EXPERIMENT #. EXPERIMENT NAME**

**Submitted by:**

**on:**

**to :** Dr. Onur Tigli

**Objective**

**Verilog HDL and Testbench Code**

The filename of your group zip file that contains all of your code.

**Simulation Results**

**Device Utilization Summary**

**RTL Schematic and Technology Schematic**

**Post Place and Route Static Timing**

**Post Place and Route Simulation Results**

**Power Data**

**Conclusions**

Discuss very briefly the results based on the questions asked in the lab handout. Report any discrepancies you observed, problems you encountered and solutions you applied.

**References**

[1]: list any resource that you used (data sheet, website, book, database)